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Attorney's Docket 021906-0307405  
Client Reference: PIA31178/DBE/US-HO

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re PATENT APPLICATION of:  
JI MYONG LEE

Confirmation No: 9617

Application No.: 10/743,454

Group Art Unit: 2823

Filed: December 23, 2003

Examiner: ESTRADA, MICHELLE

Title: METHOD FOR PLANARIZING A SURFACE OF A SEMICONDUCTOR WAFER

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

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401 Dulany Street  
Alexandria, VA 22314

Sir:

In response to the Office Action dated July 11, 2005, please consider the following remarks in connection with the above-identified application: